

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Not for submission under 37 CFR 1.99)

Application Number	10686316
Filing Date	2003-10-15
First Named Inventor	Peter L. Montgomery
Art Unit	2183
Examiner Name	
Attorney Docket Number	MS1-1648US

U.S. PATENTS

Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	6202077		2001-03-13	Smith	

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	1	WO0176132			2001-10-11	OREGON STATE (US); KOC CETIN K (US); SAVAS ERKAY (<input type="checkbox"/>

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1	Groszschadl et al., "Architectural Enhancement for Montgomery Multiplication on Embedded RISC Processors" Lecture Notes in Computer Science-Applied Cryptography and Network Security, vol. 2846, 2003, pp. 418-434, Retrieved from the Internet: www.springerlink.com/media/h4d4wlvwvq7r9ellmv56/contributions/9/0/d/q/90dqqk117ttj7xy4g.pdf	<input type="checkbox"/>
2	Schwemmlin et al., "RNS-Modulo Reduction Upon a Restricted Base Value Set and its Applicability to RSA Cryptography", Computers & Security, Elsevier Science Publishers. Amsterdam, NL, vol. 17, no. 7, 1998, pp. 637-650	<input type="checkbox"/>
3	EPO Communication with Search Report dated March 1, 2006, from counterpart EP patent application, European Patent Application No. 04 018 745.2, copy attached, 4 pages.	<input type="checkbox"/>

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CERTIFICATION STATEMENT

Please see 37 CFR 1.97 and 1.98 to make the appropriate selection(s):

☒ None

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